CHICO STATE UNIVERSITY

ASSESSMENT SUMMARY UPDATE

PROGRAM: M.S. Electrical and Computer Engineering

Year of review	Student Learning Outcome	Describe assessment activity done this year for this SLO	Findings	Based on the results or evidence, what action was taken regarding program improvements?
2016-2017	SLO 1) An ability to identify and formulate requirements for advanced systems	(a) Embedded assessment was conducted in EECE 643 (b) No data is available from the graduate exit survey.	(a) Students achieved 100% pass rate in this assessment. (b) No data is available from the graduate exit survey.	(a) All SLOs will be assessed in EECE courses identified in the matrix annually. (b) 2017-2018 graduate exit survey will be reinstituted sand will include a question to obtain students' self-assessment of this outcome.
	SLO 2) An ability to analyze and prioritize requirements and constraints in order to determine the features that must be included in advanced systems	(a) This learning outcome was not assessed during AY 2016-2017. (b) No data is available from the graduate exit survey.	(a) This learning outcome was not assessed during AY 2016-2017. (b) No data is available from the graduate exit survey.	(a) All SLOs will be assessed in EECE courses identified in the matrix annually. (b) 2017-2018 graduate exit survey will be reinstituted sand will include a question to obtain students' self-assessment of this outcome.
	SLO 3) An ability to design advanced digital or analog systems that meet all requirements	(a) Assessment was based on the pass rate of students on the comprehensive exam. b) No data is available from the graduate exit survey.	(a) Student achieved 81% pass rate in this assessment. (b) Graduating senior survey indicates an average score of 4.50/5.00.	(a) All SLOs will be assessed in EECE courses, projects, theses, and comprehensive exams identified in the matrix annually. (b) 2017-2018 graduate exit survey will be reinstituted sand will include a question to obtain students' self-assessment of this outcome.
	SLO 4) An ability to implement designs for digital or analog systems and evaluate how well the designs meet the requirements	(a) Embedded assessment was based design assignments in EECE682. b) No data is available from the graduate exit survey.	(a) Students achieved 100% pass rate in this assessment. (b) No data is available from the graduate exit survey.	(a) All SLOs will be assessed in EECE courses identified in the matrix annually. (b) 2017-2018 graduate exit survey will be reinstituted sand will include a question to obtain students' self-assessment of this outcome.
	SLO 5) An ability to conduct literature research and assess its impact on electrical and/or computer engineering issues	(a) This learning outcome was not assessed during AY 2016-2017. (b) No data is available from the graduate exit survey.	(a) This learning outcome was not assessed during AY 2016-2017. (b) No data is available from the graduate exit survey.	(a) All SLOs will be assessed in EECE courses identified in the matrix annually. (b) 2017-2018 graduate exit survey will be reinstituted sand will include a question to obtain students' self-assessment of this outcome.
	SLO 6) An ability to apply current technologies and use modern tools to solve engineering problems	(a) Embedded assessment was conducted in EECE 482. (b) No data is available from the graduate exit survey..	(a) Students achieved 100% pass rate in this assessment. (b) No data is available from the graduate exit survey.	(a) All SLOs will be assessed in EECE courses identified in the matrix annually. (b) 2017-2018 graduate exit survey will be reinstituted sand will include a question to obtain students' self-assessment of this outcome.